Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A display apparatus for use with a host computer system, the display apparatus comprising circuitry allowing an interlaced mode of operation and a noninterlaced mode of operation, the display apparatus comprising:

a screen, said screen operable to display visually detectable output from the host computer system when operating in the noninterlaced mode of operation and operable to display a television compatible signal when operating in the interlaced mode of operation, said noninterlaced mode and interlaced mode of operation supporting an ATSC (Advanced Television Committee System) input;

a communication channel between said host computer system and said display apparatus, the communication channel for transmitting commands and information to and from said host computer system and to and from said display apparatus; and

a microprocessor ~~for receiving~~ configured to receive said commands from said host computer system when the display apparatus is in the interlaced mode of operation and when the display apparatus is in the noninterlaced mode of operation, said microprocessor comprising control logic for switching said display apparatus between said interlaced and noninterlaced modes of operation in response to at least one of said commands,

wherein the microprocessor receives at least one command from said host computer

system, the command suitable for controlling a television function of the display apparatus from

the host computer system.

2. (Original) A display apparatus of claim 1, wherein said interlaced mode of operation supports

at least one of a National Television System Committee (NTSC) input, a Phase Alteration by

Line (PAL) input, and a Sequential a Memoire (SECAM) input.

3. (Original) A display apparatus of claim 1, wherein said noninterlaced mode of operation

supports at least one of a computer graphics mode input, VGA input and SVGA input.

4. (Previously Amended) A display apparatus of claim 1, wherein the television function

includes at least one of changing a channel, volume adjustment and picture adjustment.

5. (Previously Amended) A display apparatus of claim 1, wherein the television function

includes at least one of selecting a video source, brightness, contrast, vertical and horizontal

sizing and positioning, on/off (rest/resume), refresh rate, resolution and color temperatures.

6. (Original) A display apparatus of claim 5, wherein the television function of the display

apparatus is controlled from the host computer system while the display apparatus is in an

interlaced mode of operation.

7. (Previously Presented) A display apparatus of claim 1, wherein when said display apparatus is switched to said interlaced mode of operation, a video signal from a video controller in noninterlaced mode is not displayed by said display apparatus.

8. (Original) A display apparatus of claim 1, wherein said interlaced mode of operation supports Sequential a Memoire (SECAM) input.

9. (Original) A display apparatus of claim 1, wherein the command is a display mode change command.

10. (Original) A display apparatus of claim 9, wherein the command is sent over a serial port.

11. (Original) A display apparatus of claim 9, wherein the command is sent over a parallel port.

12. (Original) A display apparatus of claim 1, wherein the microprocessor enables an overlay window on the display apparatus.

13. (Original) A display apparatus of claim 12, wherein the overlay widow is enabled as at least one of a picture-in-picture (PIP) and a picture-on-picture (POP).

14. (Currently Amended) A computer system comprising:

     a host computer system including:

a processor;

a memory coupled to said processor;

a video controller coupled to said processor and said memory; and

a display apparatus coupled to a video controller of the host computer system, the display apparatus comprising circuitry allowing an interlaced mode of operation and a noninterlaced mode of operation, the display apparatus comprising:

a screen, said screen operable to display visually detectable output from the host computer system when operating in the noninterlaced mode of operation and operable to display a television compatible signal when operating in the interlaced mode of operation, said interlaced mode of operation and noninterlaced mode of operation supporting an ATSC (Advanced Television Committee System) input;

a communication channel between said host computer system and said display apparatus, the communication channel for transmitting commands and information to and from said host computer system and to and from said display apparatus; and

a microprocessor ~~for receiving~~ <u>configured to receive said</u> commands from said host computer system <u>when the display apparatus is in the interlaced mode of operation and when the display apparatus is in the noninterlaced mode of operation</u>, said microprocessor comprising control logic for switching said display apparatus between said interlaced and noninterlaced modes of operation in response to <u>at least one of</u> said commands,

wherein the microprocessor receives at least one command from said host computer system, the command suitable for controlling a television function of the display apparatus from the host computer system.

15. (Original) A computer system of claim 14, wherein said noninterlaced mode of operation supports at least one of computer graphics mode input, VGA input and SVGA input.

16. (Previously Amended) A computer system of claim 14, wherein the television function includes at least one of changing a channel, volume adjustment and picture adjustment.

17. (Previously Amended) A computer system of claim 14, wherein the television function includes at least one of selecting a video source, brightness, contrast, vertical and horizontal sizing and positioning, on/off (rest/resume), refresh rate, resolution and color temperatures.

18. (Original) A computer system of claim 17, wherein the television function of the display apparatus is controlled from the host computer system while the display apparatus is in an interlaced mode of operation.

19. (Original) A computer system of claim 14, wherein said display apparatus is switched to said interlaced mode of operation, a video signal from a video controller in noninterlaced mode is not displayed by said display apparatus.

20. (Original) A computer system of claim 14, wherein the video controller receives a signal from the display apparatus.

21. (Original) A computer system of claim 20, wherein the signal from the display apparatus is a video signal.

22. (Original) A computer system of claim 21, wherein the video signal is a composite signal.

23. (Original) A computer system of claim 21, wherein the video signal is an S-video signal.

24. (Original) A computer system of claim 14, wherein said interlaced mode of operation supports at least one of a National Television System Committee (NTSC) input, a Phase Alteration by Line (PAL) input, and a Sequential a Memoire (SECAM) input.

25. (Original) A computer system of claim 14, wherein the command is a display mode change command.

26. (Original) A computer system of claim 25, wherein the command is sent over a serial port.

27. (Original) A computer system of claim 25, wherein the command is sent over a parallel port.

28. (Original) A computer system of claim 25, wherein the command is sent over a data port.

29. (Original) A computer system of claim 14, wherein the microprocessor enables an overlay window on the display apparatus.

30. (Original) A computer system of claim 29, wherein the overlay widow is enabled as at least one of a picture-in-picture (PIP) and a picture-on-picture (POP).

31. (Currently Amended) A method of operating a computer system to control a display apparatus, the display apparatus coupled to a video controller of the computer system, said computer system and said display apparatus further coupled via a communication channel, the display apparatus comprising circuitry providing a first mode of operation and a second mode of operation, said first mode of operation being one of an interlaced mode of operation and a noninterlaced mode of operation and the second mode of operation being the other, said first mode of operation and second mode of operation supporting an ATSC (Advanced Television System Committee) input, said method comprising the steps of:

operating the display in said first display mode;

receiving user input to change the display mode from said first mode of operation to said second mode of operation;

sending a mode change command to the display apparatus in response to said user input <u>received in either the interlaced mode of operation or the noninterlaced mode of operation</u>;

transitioning the display apparatus from said first mode of operation to said second mode of operation; and

controlling at least one television function of the display apparatus from the host computer system by a command received from said host computer system when said display device is in said interlaced mode of operation,

wherein the television function includes at least one of changing a channel, volume adjustment, picture adjustment, selecting a video source, brightness, contrast, vertical and horizontal sizing and positioning, on/off (rest/resume), refresh rate, resolution and color temperatures.

32. (Original) A method of claim 31, wherein said interlaced mode of operation supports at least one of a National Television System Committee (NTSC) input, a Phase Alteration by Line (PAL) input, and a Sequential a Memoire (SECAM) input.

33. (Original) A method of claim 32, wherein said display apparatus is transitioned to said second mode of operation from said first mode of operation, a video signal from a video controller in said first mode is not displayed by said display apparatus video controller in said first mode is not displayed by said display apparatus.

34. (Original) A method of claim 32, wherein the mode change command is sent from the computer system via the communication channel.

35. (Original) A method of claim 31, wherein the video controller enables an overlay window on the display apparatus.

36. (Original) A method of claim 35, wherein the overlay widow is enabled as at least one of a picture-in-picture (PIP) and a picture-on-picture (POP).

37. (Currently Amended) A computer system comprising:

a host computer system including: a processor;

a memory coupled to said processor;

a video controller coupled to said processor and said memory; and

a display apparatus coupled to a video controller of the host computer system, the display apparatus comprising:

a screen, said screen operable to display visually detectable output from the host computer system when operating in the noninterlaced mode of operation and operable to display a television compatible signal when operating in the interlaced mode of operation, said noninterlaced mode of operation and interlaced mode of operation supporting an ATSC (Advanced Television System Committee) input;

a communication channel between said host computer system and said display apparatus, the communication channel for transmitting commands from said host computer system to said display apparatus; and

a microprocessor ~~for receiving~~ configured to receive commands from said host computer system when the display apparatus is in the interlaced mode of operation and when the display apparatus is in the noninterlaced mode of operation, said microprocessor comprising control logic for controlling a television feature of the display apparatus from the host computer system when said screen is operating in said interlaced format,

wherein the television feature includes at least one of changing a channel, volume adjustment, picture adjustment, selecting a video source, brightness, contrast, vertical and

horizontal sizing and positioning, on/off (rest/resume), refresh rate, resolution and color temperatures.

38. (Original) A computer system of claim 37, wherein said interlaced mode of operation supports at least one of a National Television System Committee (NTSC) input, a Phase Alteration by Line (PAL) input, and a Sequential a Memoire (SECAM) input.

39. (Original) A computer system of claim 37, wherein the microprocessor is suitable for switching said display apparatus between said interlaced and noninterlaced modes of operation.

40. (Original) A computer system of claim 37, wherein the microprocessor enables an overlay window on the display apparatus.

41. (Original) A computer system of claim 40, wherein the overlay widow is enabled as at least one of a picture-in-picture (PIP) and a picture-on-picture (POP).

42. (Canceled)

43. (Previously Presented) A display apparatus of claim 12, wherein the display apparatus permits the utilization of other computer functions on at least one underlying screen of the overlay window.

44. (Canceled)

45. (Previously Presented) A computer system of claim 29, wherein the computer system permits the utilization of other computer functions on at least one underlying screen of the overlay window.

46. (Canceled)

47. (Previously Presented) A method of claim 35, wherein further comprising permitting the utilization of other computer functions on at least one of underlying screens of the overlay window.

48. (Canceled)

49. (Previously Presented) A display apparatus of claim 40, wherein the computer system permits the utilization of other computer functions on at least one of underlying screens of the overlay window.

50. (New) A display apparatus of claim 1, wherein said commands from the host computer system are sent via a bidirectional display data channel (DDC) interface.

51. (New) A computer system of claim 14, wherein said commands from the host computer system are sent via a bidirectional display data channel (DDC) interface.

52. (New) A method of claim 31, wherein said mode change command to the display apparatus is sent via a bidirectional display data channel (DDC) interface.

53. (New) A computer system of claim 37, wherein said commands from the host computer system are sent via a bidirectional display data channel (DDC) interface.